REMARKS

Applicants thank the Examiner for allowing claims 5-10.

Claims 1-4 have been rejected under 35 USC 112, first paragraph, as failing to comply with the enablement requirement. Applicants respectfully traverse this rejection.

Claim 1 states that each of the pixels of a display device has a serial-to-parallel converter converting a serial digital image signal supplied serially to the display device to a parallel digital image signal, a DA converter converting the parallel digital image signal to an analog image signal, and a pixel electrode configured to receive the analog image signal. The Examiner contends that this structure is not described in the specification in such a way as to enable persons skilled in the art to make and use the claimed invention. Applicants respectfully disagree.

To support the rejection, the Examiner states at paragraph 1 of the Action, "By looking at figure 1, each pixel electrode (19) does not have a serial to parallel converter connected and also a DA converter connected to a single pixel electrode (19 In fig. 1)." This statement is by itself correct. However, it does not support the Examiner's contention that the specification fails to enable the claimed pixel.

Pixel electrode 19 includes neither the serial-to-parallel converter nor the DA converter, as the Examiner points out. Rather, the pixel itself, not the pixel electrode 19, has the two converters, as shown in FIG. 1 of the application. The specification explains at page 3, lines 22-24, "Fig. 1 is a circuit diagram of the liquid crystal display device of the first embodiment. Although only a single pixel is shown in the figure for simplification, a plurality of the pixels is arranged in a matrix in the display device." Thus, the single pixel shown in FIG. 1, which has the same structure as the other pixels of the display device, includes the serial-to-parallel converter, which is comprised of pixel selecting transistors GT0-GT3 connected to drain signal line 2, shift resistors SR supplying a sample signal to the gates of the pixel selection transistors and capacitor CS0-CS3 storing bit signals of the serial digital image signal. The pixel of FIG. 1

also includes a DA converter, which is comprised of the pixel electrode 19 and capacitor electrodes 41-44 that form capacitance coupling with the pixel electrode 19. See, for example, page 3, line 25 - page 5, line 16, of the specification.

Based on the disclosures in the specification described above, persons skilled in the art would be able to produce and use the claimed display in which each pixel has a serial-to-parallel converter and a DA converter. Thus, the rejection of claims 1-4 under 35 USC 112, first paragraph, as not enabling should be withdrawn because the specification does not lack the enablement of the claimed display device.

Applicants note that claim 5 states that the display device, not the pixel as in claim 1, has the shift resistors. Accordingly, the structure of claim 5 includes the configuration in which two or more pixels share the same shift resistors.

In the event that the transmittal is separated from this document and the Patent and Trademark Office determines that an extension and/or other relief is required, applicants petition for any required relief including extensions of time and authorize the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952**, referencing Docket No. **492322013500**.

Respectfully submitted,

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